

Nanotechnology and Molecular Computation (IJNMC)

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EDITOR-IN-CHIEF BIO

Bruce MacLennan received a BS (1972) in mathematics (with honors) from Florida State University and a MS (1974) and PhD (1975) in computer science from Purdue University. He joined Intel Corporation in 1975 where as a senior software engineer, he worked on the architectures of the 8086 and the iAPX-432 processors. In 1979 he returned to academia, joining the computer science faculty of the Naval Postgraduate School (Monterey, CA), where he was an assistant professor (1979–83), an associate professor (1983–7), and Acting Chair (1984–5). At NPS he investigated novel models for massively parallel computing and artificial intelligence. Since 1987, he has been a member of the electrical engineering and computer science Faculty of the University of Tennessee (Knoxville, USA).

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All submissions and inquiries should be directed to the attention of:

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