Design and Modeling of Low Power VLSI Systems

Part of the Advances in Computer and Electrical Engineering Book Series

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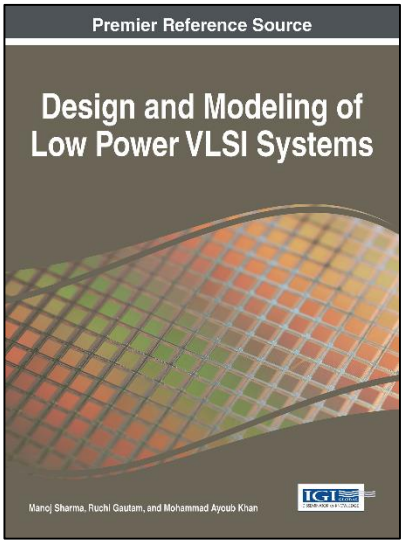

Description:

Very Large Scale Integration (VLSI) Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip. Emerging research in this area has the potential to uncover further applications for VSLI technologies in addition to system advancements.

Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization through a research-based discussion of the technicalities involved in the VLSI hardware development process cycle.

Readers:

This book is a useful resource for researchers, engineers, and graduate-level students in computer science and engineering.

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Topics Covered:

- Integrated Circuits
- Low Power Benchmarking
- Low Power Circuit Design
- Low Power Modeling
- Power Optimization
- Routing Strategies
- Testing Methodologies

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